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KG Blemel - US Patent App. 09/977,140, 2001 - Google Patents

... comprised of computing devices that use computer algorithms embodied in software programs to ... FPGA and non-volatile memory, digitizers, and other electronic hardware described in ... controller 8. Program addresses are accomplished by an address partitioning and control ...

## The Specification Language SpecC within the PARADISE Design Environment

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A Rettberg, FJ Rammig, A Gerstlauer, DD ... - ... and Design of ..., 2001 - books.google.com

... [Ha95] W. Hardt, "An Automated Approach to HW/SW-Codesign". In IEEE Colloquium: Partitioning in Hardware-Software Codesigns. London, Great Britain, February 1995. [HaReK199] W. Hardt, A. Rettberg, B. Kleinjohann. "The PARADISE design environment". In Proc. ...

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M O'Neill, M Price, RJ Richter, S Shah, G Entine, J King ... - 1990 - oai.dtic.mil

... development of this product dealt in the sophisticated ing, and probability density function accumulation. A high-level software routines optimized for the hardware large amount of onboard data and program memory environment. Through the use of these routines, the ...

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## [book] Computer-based diagnostic systems

[aber.ac.uk \[TXT\]](#)

CJ Price - 1999 - books.google.com

... Consider, for example washing machines, cars, aeroplanes, computers, software. These products are too costly to be "disposable" ... The Web address for obtaining software and other material linked to the book is "http://www.aber.ac.uk/~cjp/diagnosticbook/". ...

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NCS Handbook - JAERI-Review, 2001 - dspace.lboro.ac.uk

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Apr 29, 2010 ... **Architecture Analysis & Design Language**, TOGAF for ... **Mobile Architecture Joint Meeting with Security Forum** – **Use Case Scenarios** ... Jack Fujieda on the **Automotive Industry ... deployment of the system (hardware & software) and/or system- ...**

**Finite State Machine** Labs. □ Florida State University ... [https://www.opengroup.org/rforum/.../Rome\\_Handout\\_27-29\\_April\\_10.pdf](https://www.opengroup.org/rforum/.../Rome_Handout_27-29_April_10.pdf)

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by H Shokry  
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the **state machine** becomes a discrete-time Markov chain (DTMC). .... in software and partially in hardware (e.g., **Hardware in the Loop** (HIL) systems ... in the system design phase, e.g., in the automotive area within the tool .... The mathematical model used in MBST is a **finite state**, time homogeneous, ...

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6. Embedded.com - Software Design Books

Despite this inherent simplicity, the **use case** approach is frequently ... of the relevant concepts including traditional **finite state machines**, ... Executable UML: A Foundation for Model-Driven Architecture ... This is the first book on embedded systems to offer a unified approach to **hardware and software** ...

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... s) of Freedom Dwt Dead weight tons EC European Commission ECU European Currency ... High

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A Girault, B Lee, EA Lee - IEEE Transactions on Computer- ..., 1999 - ieeexplore.ieee.org

... berkeley.edu). Publisher Item Identifier S 0278-0070(99)03963-9. scheduling, and reasonably efficient synthesis of embedded software or hardware. ... Finite state machines (FSM's) have long been used to describe and analyze intricate control sequences. ...

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... Depending on the selected architecture, each task will take a specified number of clock to be executed (one clock ... depending on the selected target processor for software) and in different execution constraint (concurrency for hardware, mutual exclusion for software). ...

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MC Heimlich, KRS Hilaire - US Patent 5,903,886, 1999 - Google Patents

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S Narayan, F Vahid, DD Gajski - IEEE Design and Test of Computers, 1992 - computer.org

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B Hedenetz, R Belschner - SAE transactions, 1999 - vmars.tuwien.ac.at

... 9. A. Krüger, "Interface design for Time-Triggered Real-Time System Architectures", Ph.D ... 10. A. Krüger, H. Kopetz, "A Network Controller Interface for a Time-Triggered Protocol ... SAE Symposium on Future Transportation Electronics: Multiplexing and In-Vehicle Networking, SAE ...

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## Software technology in an automotive company: major challenges

K Grimm - ... of the 25th international conference on Software ..., 2003 - portal.acm.org

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the ... This platform, called Agent Network System ANTS, is based on agent technology. ...

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## Design Method for Conceptual Design of By-Wire Control: Two Case Studies

K Ahlström, J Torin, P Johannesson - iceccs, 2001 - computer.org

... This case study presents one of the **design** concepts of the brake, steering and throttle systems. ...

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E Toulioupis, JA Flint, D Ward - ... Systems and Control Division Research 2003 - [Citeseer](#)

... In this project, we try to **design** a high integrity embedded architecture capable to achieve the safety level required in drive-by-wire systems. ... [6] BOSCH's Controller Area Network [Online]. ... [11]

Development Guidelines for Vehicle Based Software, The Motor Industry Software ...

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**42 V--an indication for changing requirements on the vehicle electrical system**

K Ehlers, HD Hartmann, E Meissner - Journal of Power Sources, 2001 - Elsevier

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A Rajnak, A Kumar - 44th ACM/IEEE Design Automation ..., 2007 - [ieeexplore.ieee.org](#)

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S Grange, T Fong, C Baur - IEEE International Conference on Robotics ..., 2000 - Citeseer  
 ... 4 System design 4.1 Overview ... The user interface is connected to the rest of the system via a persistent network link. Base Station. ... Robot. The teleoperated vehicle is equipped with on-board sensors (including a pan-tilt camera) and a motion controller. ...

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User and operator perspectives in transit network design

A Ceder, Y Israeli - ... Journal of the Transportation Research Board, 1998 - Trans Res Board  
 ... It is a histogram describing the number of passengers on board the transit vehicle versus the transit ... to incorporate three operational planning elements simultaneously at the macro and micro levels: network design, frequency (timetable) setting, and vehicle scheduling ...

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Deploying a wireless sensor network on an active volcano[psu.edu \[PDF\]](#)

G Werner-Allen, K Lorincz, M Weish, O ... - IEEE Internet ..., 2006 - computer.org  
 ... Before describing our design in detail, let's take a high-level view of our sensor node ... The MSP430 microcontroller provides on-board ADCs, but they're unsuitable for our application. ... A pair of alkaline D cell batteries powered each sensor node — our network's remote location ...

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[PDF] Design guidelines for safety of in-vehicle information systems[transport-research.info \[PDF\]](#)

A Stevens, A Quimby, A Board, T ... - Prepared for the ..., 2002 - transport-research.info  
 ... Section 2 describes the different stages of the design process and considers briefly what each ... Section 3 deals with the documentation and user instructions that may need to be provided ... 4 provides guidelines (if required) about how the IVIS should be fitted within the vehicle. ...

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D Siewiorek, A Smailagic, L Bass, J Siegel, R ... - Proceedings of the 2nd ..., 1998 - Citeseer  
 ... The on-board vehicle system prototype gathers internal status data from embedded sensors in systems such as propulsion and doors. ... Page 3. impact on the overall design. ... FarSite requires a Winsock compliant Windows network interface, which is available with the WaveLAN. ...

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An autonomous guided vehicle for cargo handling applications

HF Durrant-Whyte - The International Journal of Robotics ..., 1996 - ijr.sagepub.com

... of the overall vehicle and on-board systems architecture; the development of a navigation system appropriate for large, outdoor, high-speed vehicles; and the experimental program leading to validation of the vehicle system. 1. Introduction This article describes the design of an ...

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[PDF] Robotic ocean vehicles for marine science applications: the european asimov project

uac.pt [PDF]

A Pascoal, P Oliveira, C Silvesbre, L Sebastião ... - OCEANS- ..., 2000 - horta.uac.pt

... The vehicle is a major re-design of the MARIUS AUV, developed under the MAST-I and ... will be compiled, downloaded to, and run in real time on the computers installed on-board the ASC ... viewing all the modules inside each one of the vehicles as nodes in a local area network ...

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### On-board network for motor vehicles

A Moller, H Reimann, B Schurmann, G Wiltz - US Patent 4,942,571, 1990 - Google Patents

... These harnesses are of very complex design and are prone to manufacturing errors since the fabrication of a harness is done predominantly manually. ... It has therefore been proposed already to replace the wire-harness of a motor vehicle by an on- board network, in which ...

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### Design of automatic guided vehicle systems

WL Maxwell, JA Muckstadt - IIE Transactions, 1982 - informaworld.com

... accomplished using some type of guide path in or on the floor, on-board computers or ... a system and wants to determine how many vehicles are needed to support that design. ... Only the issue of vehicle requirements must be transportation network corresponding to this problem is ...

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### A large-scale complex virtual environment for team training

TW Mastaglio, R Callahan - Computer, 1995 - ieeexplore.ieee.org

... Potential applications are shared design and other cooperative activities, long- distance telecommunications or ... a combat vehicle simulator, the crew has the same vehicle and on-board weapons controls and ... data packets transmitted over the FDDI LAN from all network entities. ...

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SJ Salom - 1995 - ieeexplore.ieee.org

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**The Chinook hardware/software co-synthesis system**[psu.edu \[PDF\]](#)

PH Chou, RB Ortega, G Borriello - Proceedings of the 8th ..., 1995 - portal.acm.org

... To enable designers to rapidly evaluate different architectural templates and partitionings, Chinook facilitates migration of functionality among processing elements and ... Constrained software generation for HW/SW systems. ... The Verilog **Hardware Description Language**. ...

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**Platform-based design and software design methodology for embedded systems**[psu.edu \[PDF\]](#)

A Sangiovanni-Vincentelli, G Martin - 2001 - ieeexplore.ieee.org

... architecture platform must be abstracted at a level where the application **software** can use a high-level **interface** to the hardware: the API ... A **software** layer performs this abstraction, as shown in Figure 1 (next page) ... This layer wraps the essential parts of the architecture platform: ...

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**[book] Hardware-software co-design of embedded systems: the POLIS approach**

F Balarin, P Di Giusto, A Jurecska, M Chiodo, C ... - 1997 - books.google.com

... 226 5.4 The architecture selection user interface ... Lavagno then became the key architect of the POLIS system. ... xviii **Hardware-Software Co-design of Embedded Systems** ■ Cadence, and in particular, Patrick Scaglia, Vice-President of Research, Jim Rowson, Fellow, of the Aha ...

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DE Thomas, JK Adams, H Schmit - IEEE Design and Test of ..., 1993 - computer.org

... In a software implementation, memory bandwidth is limited to that supported by the CPU and memory subsystem. A **hardware** implementation can achieve high memory bandwidth at low cost by employing a memory architecture tailored to the application. ...

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**[PDF] Hardware-software co-design of embedded systems**[tkk.fi \[PDF\]](#)

WH Wolf - Proceedings of the IEEE, 1994 - users.tkk.fi

... The Echelon Neuron architecture also uses a medium-performance serial link between the processors. ... Embedded software can usually be thought of as a system of communicating processes, though the ... a sequential machine, which we will use to refer to either a **hardware** or ...  
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[escherinstitute.org \[PDF\]](#)

Á Lédeczi, A Bakay, M Maroti, P Volgyesi, G ... - Computer, 2001 - ieeexplore.ieee.org  
... GME's most important distinguishing characteristic is the consistent application of a metalevel architecture enforc- ing the ... (computer hardware and software ... Various research groups have developed environ- ments exclusively targeting embedded software mod- eling formalisms ...  
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[caltech.edu \[PDF\]](#)

WH Mangione-Smith, B Hutchings, D Andrews, A ... - Computer, 1997 - ieeexplore.ieee.org  
... refining the emerging archi- lectures, producing more effective software/hardware APIs, better ... for application development that incorporate the models of hardware reconfiguration, and ... Mirsky and A. DeHon, "MATRIX: A Reconfigurable Computing Architecture with Configurable ...  
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### [PDF] A classification and comparison framework for software architecture description languages

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N Medvidovic, RN Taylor - IEEE Transactions on software engineering, 2000 - Citeseer  
... It is a mechanism for abstracting and reusing component interface elements. ... 5. As Melah is used to specify both the software and the hardware architecture of an application, system is a hard- ware construct, while application pertains to both. Page 16. 16 ...  
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TJ Koo, B Sinopoli, A Sangiovanni-Vincentelli, S ... - Proc. of IEEE ..., 1999 - Citeseer  
... Depending on the selected architecture, each task will take a speci ed number of clock to be executed (one clock ... depending on the selected target processor for software) and in di erent execution constraint (concurrency for hardware, mutual exclusion for software). ...  
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Figure 6: HW/SW-Interface ...

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automotive control and consumer electronics. Since various

processing blocks ... Numerous work [1-8] on codesign system

architectural .... codes which contain both control **FSM** and

interface circuitry. ...

[doi.ieeecomputersociety.org/10.1109/ISSS.1998.730601](http://doi.ieeecomputersociety.org/10.1109/ISSS.1998.730601)

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T Nieva, A Fabri, A Benammour - 2nd International Symposium on ..., 2000 - CiteSeer

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C ESCA - Proceedings, 1996 - IEEE Computer Society Press

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WW Owen, JW Rozenblit, C ESCA, ... - IEEE Symposium and ..., 1996 - ieeexplore.ieee.org

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Q Li - 2000 - IEEE Computer Society

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BP Douglass - 1999 - books.google.com

... Lau, The An of Objects: Object-Oriented Design and Architecture LeffingwellAVidrig, Managing ...

Process for Specifying Component-Based Software Szyperski, Component Software, 2E: Beyond ...

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H Van Vliet, H Van Vliet, JC Van Vliet - 1993 - Citeseer

... 291 11.5 Software Architecture Assessment. .... Software was mostly developed by hardware vendors and given away for free. ... Sometimes, software can be used as a vehicle for new technologies, such as typesetting, the production of chips, or manned space trips. ...

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... Department of Electrical Engineering - Systems University of Southern California Los Angeles, CA 90089 ... Whenever a condition is found to violate the protocol specifications in a controller ... workloads to determine the most promising designs, under the shortest possible design ...

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IR Kendall, RP Jones - *Control Engineering Practice*, 1999 - Elsevier

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G Koch, U Weinmann, U Kebischull, W ... - *Microprocessors and ...*, 1996 - Elsevier

... The design flow addressed by COBRA starts with a system level specification of a ... J. Hughes, FPGA based AS1C Hardware Emulator Architectures, School of Electrical and Computer ... 4th Eurochip Workshop on VLSI Design Training, Toledo, Spain, September 29October 1, 1993 ...

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3. [Hardware architecture - Wikipedia, the free encyclopedia](#)

A modern automobile runs vastly more **software** than the Apollo spacecraft. ... AKA human computer interface, or HCI; formerly called the man-machine interface. ... be used to alter the sequential states of a suitable (**hardware**) machine. However, a **hardware** machine and its **software** program can be designed to perform ... [en.wikipedia.org/wiki/Hardware\\_architecture](http://en.wikipedia.org/wiki/Hardware_architecture) - Cached - Similar

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Sep 9, 2009 ... Figure 1: The AUTOSAR ECU incorporates standardized elements developed with ... The **AUTOSAR software topology** encourages a top-down design approach. .... **AUTOSAR** is a promising new automotive **network architecture** that brings with ... passing between otherwise incompatible **hardware interface layers** ...

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Major **software** evolutions occur when the full ...

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Jul 18, 2010 ... Just like the services, the **ECU's I/O interfaces** are also represented ... Thanks to the systematic **hardware** abstraction offered by AUTOSAR the **TIER1** ... the service **mapping** for all diagnostic services of the new OEM ... enables early validation of the **ECU software architecture**. ... EE Times **Network** ...

[www.eetimes.com/.../AUTOSAR-New-paths-to-ECU-software--part-2-Cached](http://www.eetimes.com/.../AUTOSAR-New-paths-to-ECU-software--part-2-Cached)

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... unit in the early 70's, injection and ignition were integrated into one single electronic control unit during the ... The introduction of electronic throttle control (ETC) as a drive-by-wire system with its ... timing and cylinder individual fuel cut-off could be described in a five dimension map. ...

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[45] Date of Patent: 5,458,673 Oct. 17, 1995 [54] EXHAUST GAS PARTICULATE

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... These improvements do not require alternative wiring harness changes and do not affect ... micro-controller 8. Program addresses are accomplished by an address partitioning and control ... with slave serial and selectMAP functions for multiple FPGA program mapping and control. ...

Heating apparatus for vehicle

T Morikawa, H Ito, S Aoki, G Uchida, Y Kato, N ... - US Patent ..., 1999 - Google Patents

... 5. FIG. 9 is a flowchart of a segment of a program related to an engine ECU (electronic control

unit) in FIG. 5. FIG. ... 1, 3, and 4. FIG. 11 is a flowchart of a segment of a program related to an engine

ECU (electronic control unit) in a second embodiment of this invention. FIG. ...

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A Panday, D Couderc, S Marichalar - ... of the conference on Design, ..., 2001 - portal.acm.org

... This component provides a generic **software** layer suitable for all **hardware** architectures. ... Once the **architecture** for a given vehicle is established, fully described and physically placed, the compilation chain for each **ECU** freezes application **software** and middleware ...

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J Axelsson - Proceedings of the 2nd IEEE International Symposium ..., 1999 - Citeseer

... The big challenge is to combine the use of higher level implementations with small **hardware** resources. ... In the uppermost, functional, layer, the underlying **architecture** is not considered at all. Then, in the **software** layer, the same function has been partitioned, ie parts of it have ...

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U Freund, A Burst, EGH Stuttgart - Workshop on Object-oriented ..., 2000 - cs.unibw.de

... Being an **architecture** description language [5][8], the **TITUS** design methodology comprises **design** ... Since in automotive **software** resources are always allocated statically and dynamic instantiation is ... Client/server classes are independent from **hardware** and can be assigned to ...

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L UMR7503 - 2000 IEEE International ..., 2000 - Institute of Electrical & Electronics ...

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M BMW AG - IFAC proceedings series, 1984 - books.google.com

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S Hazari - THE Journal (Technological Horizons In Education), 1995 - questia.com

... Changes to **network** topology, **hardware** and **software** must be documented and maintained on a continual basis ... Client/Server: **Architecture** where computing responsibility is distributed between a front-end and back-end program. ...

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J Bortolazzi, T Hirth, T Raith - ... of the conference on European design ..., 1996 - portal.acm.org

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## Architectural trends in automotive electronics

U Kiencke, T Kytölä, KJ Neumann - Annual Reviews in Control, 1996 - Elsevier

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J Buck, S Ha, EA Lee, DG Messerschmitt - International Journal of ..., 1994 - Citeseer

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L Maciaszek - 2007 - Pearson Education

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... A screenshot of the GUI is shown in Figure 3. With Koski GUI, it is ... The library also contains both

hardware and software that is utilized for profiling the state ... Similarly, the design automation library contains hardware monitors for evaluating the hardware-related performance ...

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B Hutchings, P Bellows, J Hawkins, S Hemmert, ... - Proceedings of the ..., 1999 - Citeseer  
... libraries), including: the actual circuit description, the run-time control, the GUI (if there ... Finally, inheritance provides a natural way of manually partitioning classes into software and hardware ... therefore, the core structural design class, Logic class, provides a quick-hardware API to ...

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SH Do, NP Suh - Proceedings of ICAD2000, Cambridge, MA, 2000 - axiomaticdesign.com  
... that used for hardware systems and components, and thus the integration of software and hardware design becomes a ... DP1: Element characteristics DP2: GUI with window ... Whenever the software designer categorizes module groups as classes using the full design matrix, they ...

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NP Suh, SH Do - CIRP Annals-Manufacturing Technology, 2000 - Elsevier  
... Acclaro is an interactive software system for designers of hardware, software, and systems. ... Support ease of use while Graphical User using software Interface (GUI) Provide efficient ... The Acclaro software design has nine level hierarchies and well over 1000 leaves, which may ...

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F Belli - issre, 2001 - computer.org  
... 1 (easy to understand, but in- formal presentation of the GUI) into Fig. ... Monitoring requires a special structure of software which must be designed carefully, considering the methods and principles of the modern Software Engineering (Design for Testability). ...

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J Gerhardt, H Höninger, H Bischof - SAE transactions, 1998 - [cloudy.cl.funpic.de](#)

... Unit in the early 70's, injection and ignition were integrated into one single electronic control unit during the ... The introduction of electronic throttle control (ETC) as a drive-by-wire system with its ... timing and cylinder individual fuel cut-off could be described in a five dimension map ...

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... Keywords: code-generation, electronic control unit, software development tools ... which intend to replace mechanical solutions by electronic ones, eg drive/brake by wire, enforce this ... the other hand prevents a target-specific implementation of the code from a new mapping of this ...

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KG Blemel - US Patent App. 09/977,140, 2001 - [Google Patents](#)

... These improvements do not require alternative wiring harness changes and do not affect ... micro-controller 8. Program addresses are accomplished by an address partitioning and control ... with slave serial and selectMAP functions for multiple FPGA program mapping and control. ...

[\[PDF\]](#) [THE UNIVERSITY OF CALGARY](#)

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R Mehrotra - 1998 - [dspace1.acs.ucalgary.ca](#)

... TBI TDC Air-fuel ratio (mass basis) Stoichiometric air-fuel ratio Bottom dead center

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... Hence, ChaRy supports the mapping of controller models to a number of tasks (partitioning), the extraction of their computation times (timing analysis), and the assignment of tasks to a processor network ... In IEEE Colloquium: Partitioning in Hardware-Software Codesigns. ...

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M Itoh, K Yamada, H Yoshida, K Ohtake, Y ... - US Patent ..., 1993 - Google Patents

... The motor vehicle output power control system includes an electronic control unit for controlling an actuator to lower the output drive torque of the engine, independent ... Ada 9X Program Structure and Compilation Issues, Draft Mapping Document, Feb. ... "System Partitioning and Its ...

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LE Porter, W Merrill - ... on Industrial & Engineering Applications of ..., 1988 - books.google.com

... 3. The mapping of the state to hypotheses is neither unique nor one-to-one mapping. ...

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water, sewer and stormwater network design software Bentley Systems Europe B V .... + ECU Software + Process Management + Solutions for... + Hardware ... The convenient and easy-to-operate user interface of the Network Designer CAN ... to interactively design the FlexRay schedule forms the basis for the tool. ...

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the **harness** (or wiring) domain responsible for **designing** the wire harness .... To illustrate, the ECU design data 219 may be formatted for use by a .... The **network interface** device 320 can provide connectivity to a **network** 326, e.g., ..... repartitioning the **distribution** of tasks and functions between **hardware** ...

[www.faqs.org/patents/app/2010031212](http://www.faqs.org/patents/app/2010031212) - Cached - Similar

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Jun 30, 2010 ... New ECU and bus **interface** modules. The new compact **hardware** modules of the ... A new approach for process-oriented and tool based automation of calibration tasks ... Thanks to the open **architecture** LABCAR customers are able to ... CLONE RTPC in RTPC Web **Interface** for easy **distribution** of RTPC set up ...  
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